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Relevance scale **1** [Session S9.2: embedded programs: Predictable programs in barcodes](#)

Alwyn Goodloe, Michael McDougall, Carl A. Gunter, Rajeev Alur

October 2002 Proceedings of the 2002 international conference on Compilers, architecture, and synthesis for embedded systemsFull text available:  [pdf \(159.20 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We explore the challenges for making the programming interfaces for embedded devices open and safe, and present a prototype architecture for delivering verified programs using barcodes. In particular, we consider programs for microwave ovens, which provide a basic open API for controlling cooking times. In our architecture, recipes are written in Java, and their safety properties are formally verified using the model checker Spin. We use off-the-shelf utilities for compressing the byte code, and ...

Keywords: active barcodes, code delivery, formal verification, programmability of embedded devices

2 [Representing linear algebra algorithms in code: the FLAME application program interfaces](#)

Paolo Bientinesi, Enrique S. Quintana-Ortí, Robert A. van de Geijn

March 2005 ACM Transactions on Mathematical Software (TOMS), Volume 31 Issue 1Full text available:  [pdf \(415.14 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this article, we present a number of Application Program Interfaces (APIs) for coding linear algebra algorithms. On the surface, these APIs for the MATLAB M-script and C programming languages appear to be simple, almost trivial, extensions of those languages. Yet with them, the task of programming and maintaining families of algorithms for a broad spectrum of linear algebra operations is greatly simplified. In combination with our Formal Linear Algebra Methods Environment (FLAME) approach to ...

Keywords: Application program interfaces, formal derivation, high-performance libraries, linear algebra

3 [Cycle and Phase Accurate DSP Modeling and Integration for HW/SW Co-Verification](#)

Lisa Guerra, Joachim Fitzner, Dipankar Talukdar, Chris Schlager, Bassam Tabbara, Vojin Zivojinovic

June 1999 Proceedings of the 36th Annual Conference on Design Automation (DAC'99)

- Volume 00Full text available:  [Publisher Site](#)Additional Information: [full citation](#), [abstract](#)

We present our practical experience in the modeling and integration of cycle/phase-accurate instruction set architecture (ISA) models of digital signal processors (DSPs) with other hardware and software components. A common approach to the modeling of processors for HW/SW co-verification relies on instruction-accurate ISA models combined (i.e. wrapped) with the bus interface models (BIM) that generate the clock/phase-accurate timing at the component's interface pins. However, for DSPs and new mi ...

4 Software engineering #2: Specification-driven automated testing of GUI-based Java programs 

Yanhong Sun, Edward L. Jones

April 2004 **Proceedings of the 42nd annual Southeast regional conference**Full text available:  [pdf\(503.31 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents a specification-driven approach to test automation for GUI-based JAVA programs as an alternative to the use of capture/replay. The NetBeans Jemmy library provides the basic technology. We introduce a GUI-event test specification language from which an automated test engine is generated. The test engine uses the library and incorporates the generation of GUI events, the capture of event responses, and an oracle to verify successful completion of events. The engine, once genera ...

Keywords: test automation, test engine, test specification language**Results 1 - 4 of 4**

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